

NEXTGENPOL 160M160S2V0 DUAL LOOP/OUTPUT

Dual Loop/Output Voltage Evaluation Board populated with MLX160+SLX160 or MLX160+SLX040



Note: PGOOD1 may be marked as VRRDY1 or PGOOD1 on the Eval board PWB. PGOOD2 may be marked as VRRDY2 or PGOOD2 on the Eval board PWB.



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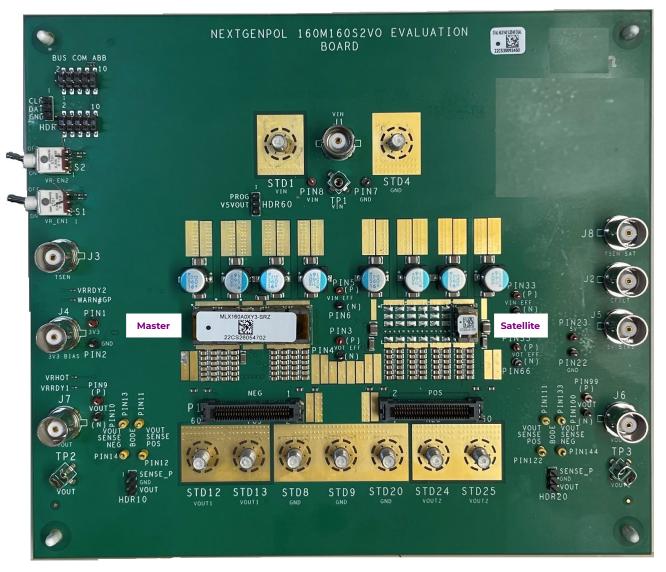


1. Description

The OmniOn Power™ MLX series are the next generation of POL modules that can deliver 40-160A; 40-160A in a two loop configured mode. It operates over a wide input range from 7V to 14Vdc and provides precisely regulated output voltage from 0.45 to 2.0V

The module's features include digital PMBus[™] interface, remote ON/OFF, output voltage sequencing, pre-biased start up, cycle-by-cycle output overcurrent protection, input and output under-voltage and over-voltage protections and over-temperature protections and more. The module has an extensive set of PMBus[™] commands for both control and monitoring of the system parameters.

The evaluation board is shown on the picture below. It comes pre-populated with required minimum of input and output capacitors. Numerous empty component place holders allow the board to be reconfigured to match a specific customer's application. Various test points facilitate the easy setup and monitoring of the module operation.



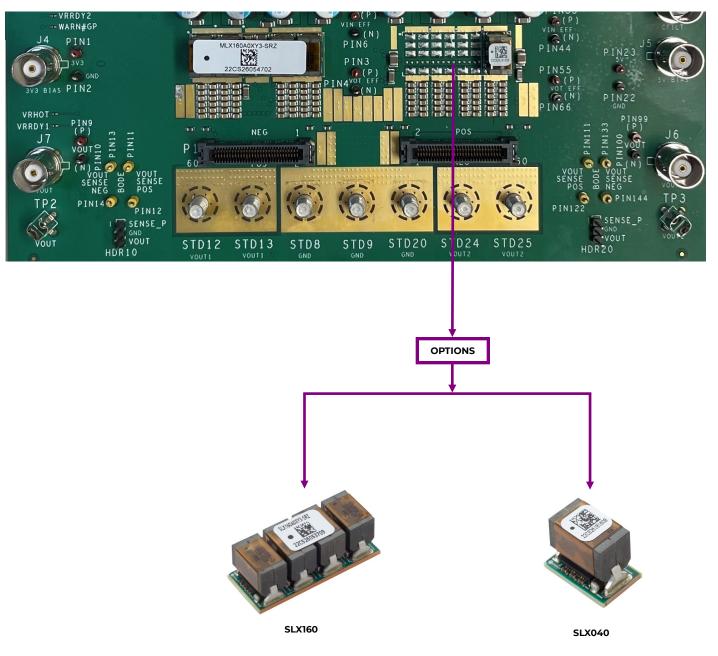
Top View of Evaluation Board with MLX160 + SLX160 moule

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1. Description (Continued)

The evaluation board can come pre-installed with any of the Satellite Modules.



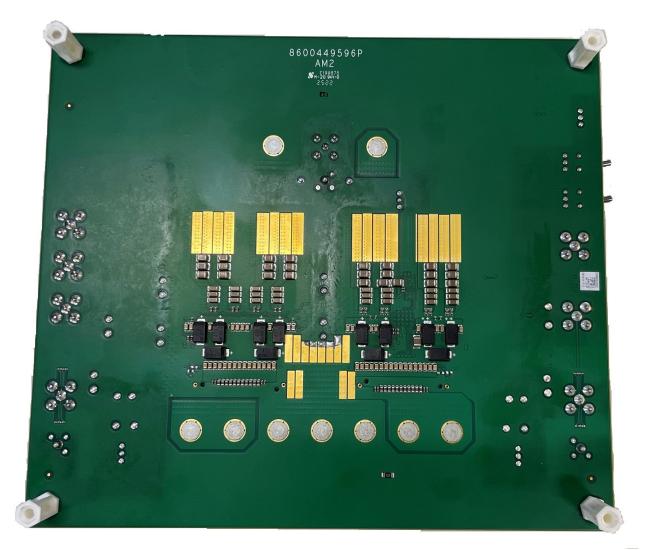
Evaluation Board with different module variants



1. Description (Continued)

The Installed components are as follows. The schematic on the following page shows maximum capability and includes expansion capability:

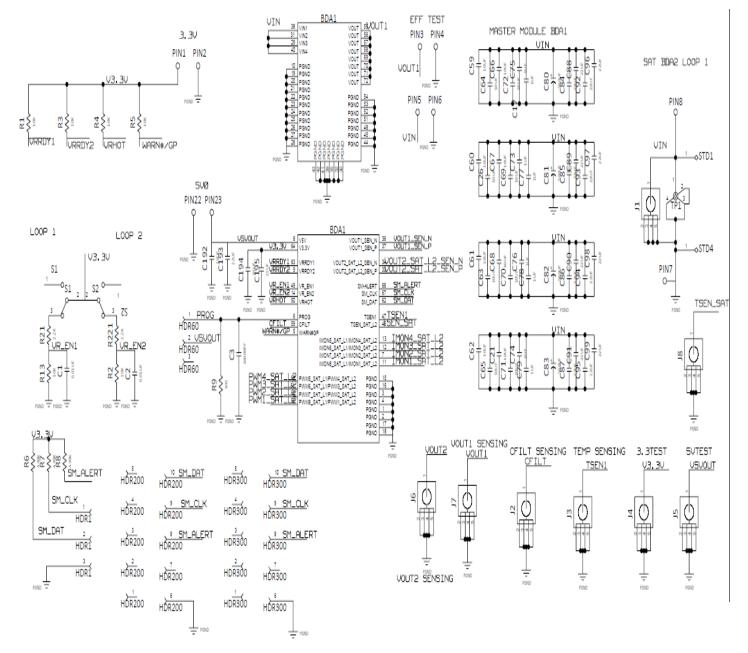
- Ceramic caps for input
- Ceramic and Surface electrolytic on output



Bottom View of Evaluation Board

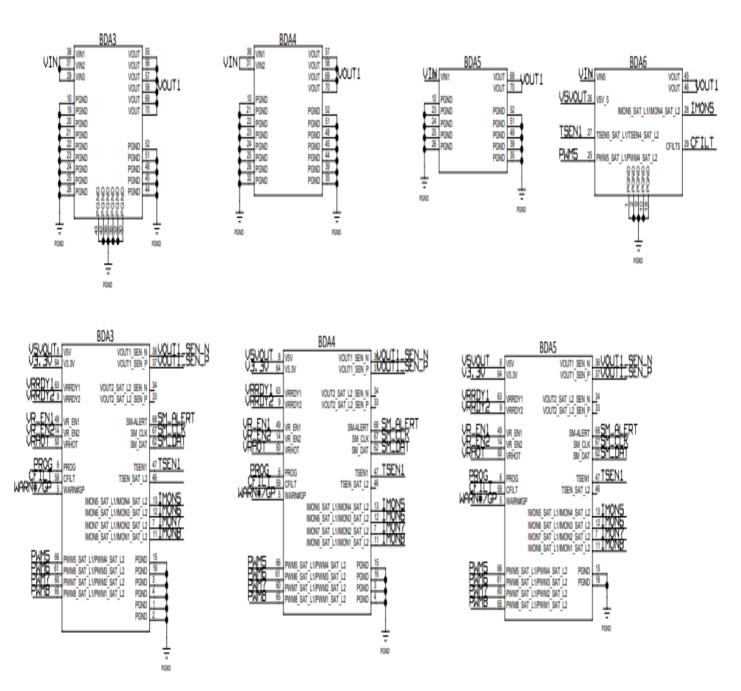


2. Schematic*



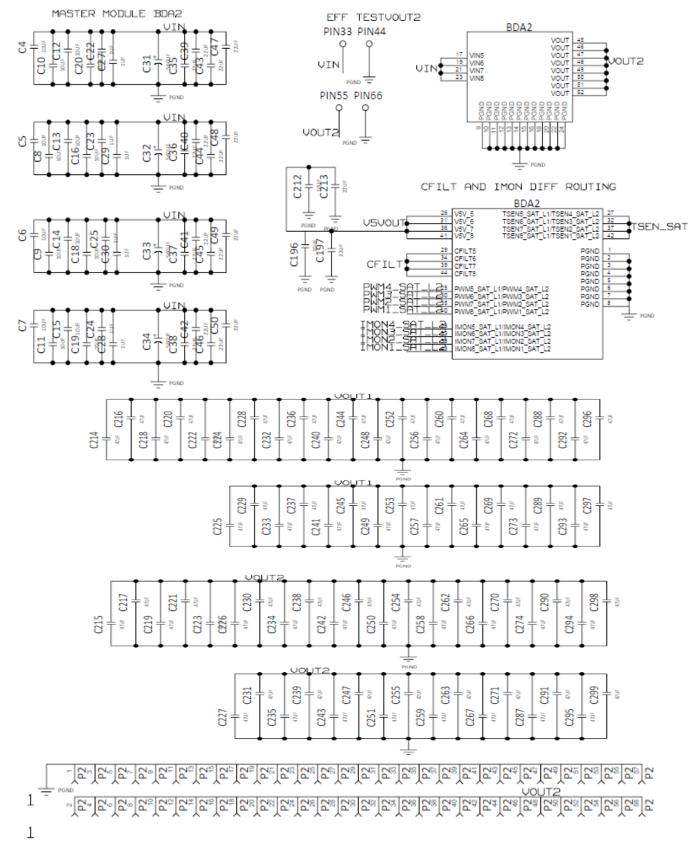
*Download Schematic at <u>www.omnionpower.com</u>.





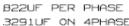
*Download Schematic at www.omnionpower.com.

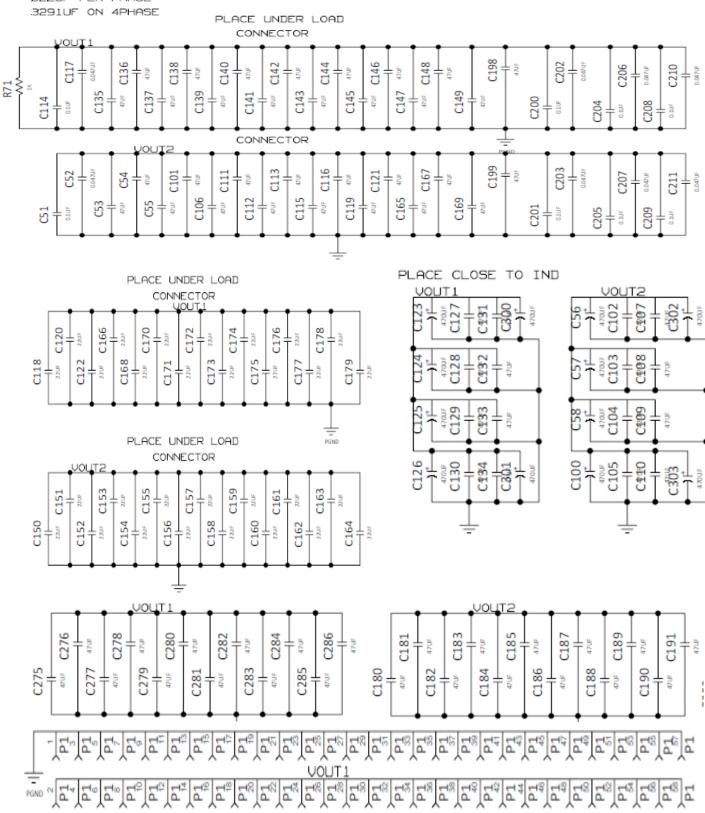




*Download Schematic at <u>www.omnionpower.com</u>.

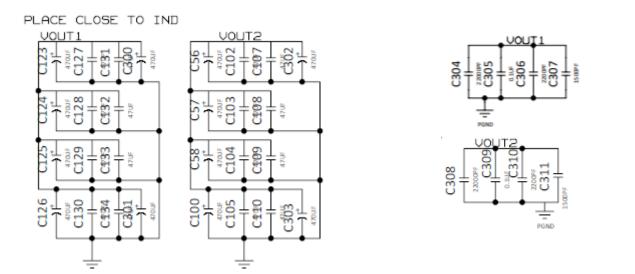


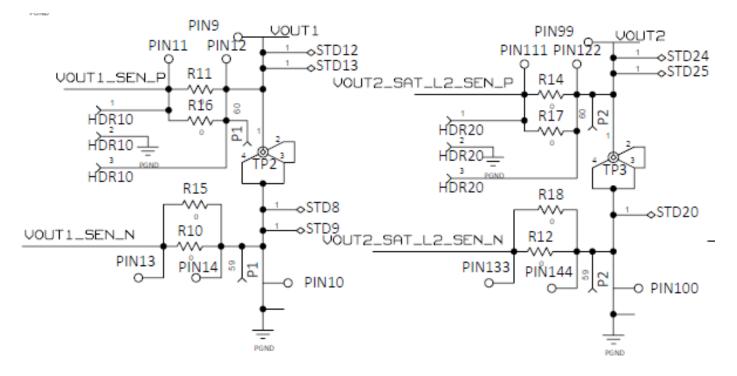




*Download Schematic at www.omnionpower.com.







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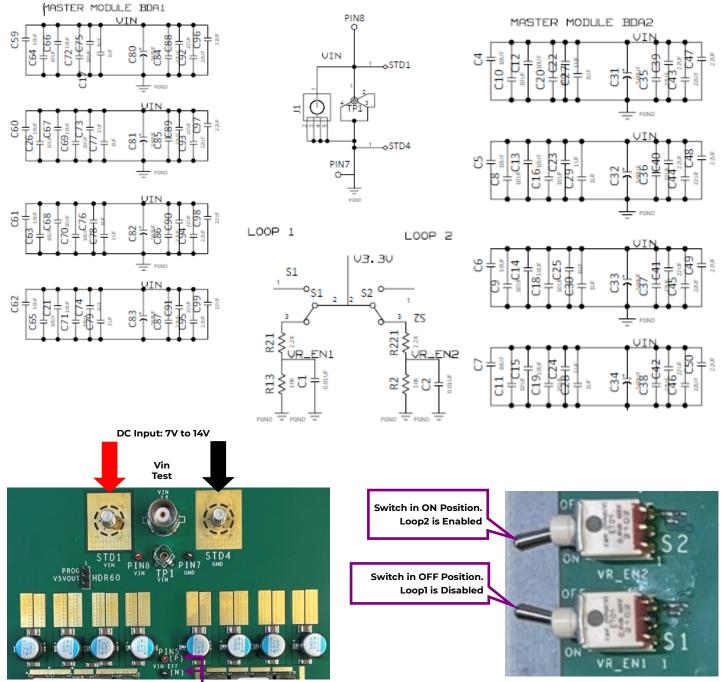


The complete schematic diagram of the MLX Series evaluation board is shown in the previous pages. Components on schematic show max capability and may not be actually used on the board. The complete schematic can be downloaded from <u>www.omnionpower.com</u>.

2.1. Eval Board Sections

The following pictures show the input connections and components external to the module.

2.1.1. Input Connections



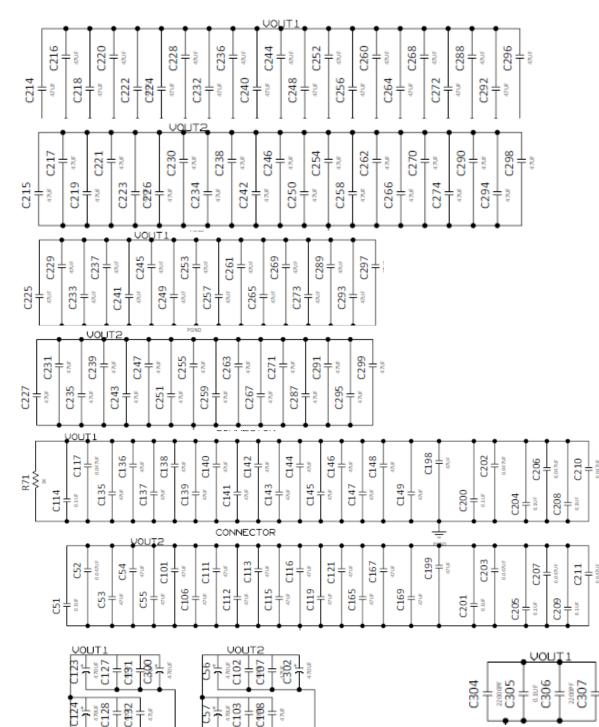
Vin Sense/ Efficiency

MLX-SLX Dual Output Evaluation Board Guide



2.1.2. Output Connections

Schematic shows max capability. Board will not be populated with all components.



C109

CHHO

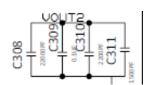
33

C104

C105

20

C100



50019

황뷶

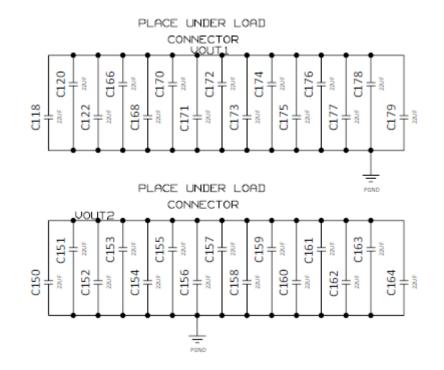
C130

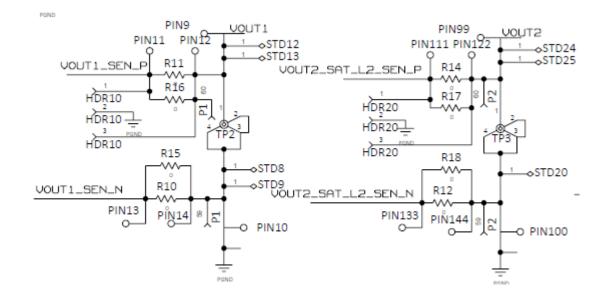
5212

C126

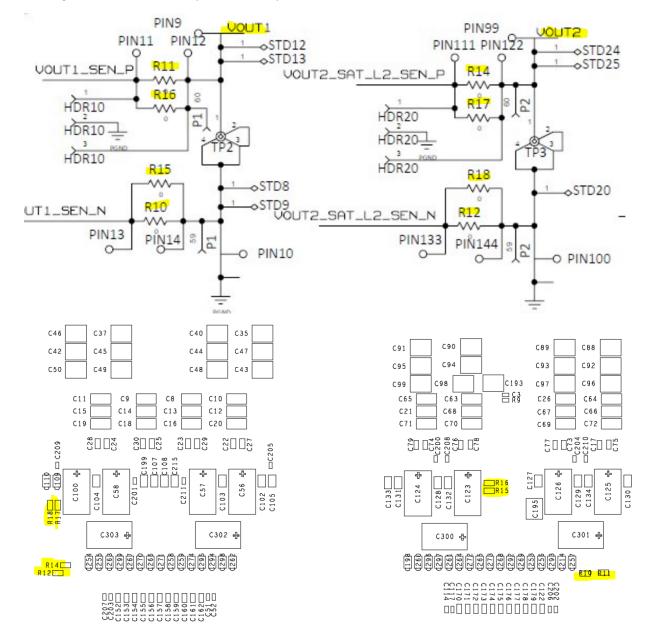


2.1.2. Output Connections (Continued)









2.1.2. Output Connections (Continued)

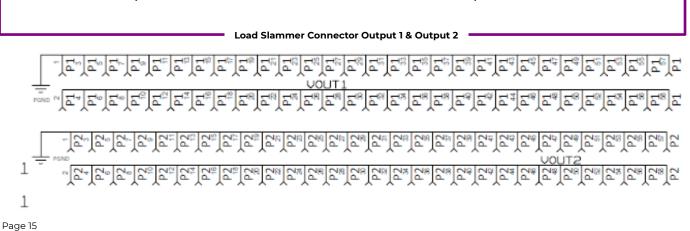
There are two set of traces for Vout sensing. Zero ohm resistors are provided to select the sensing location.

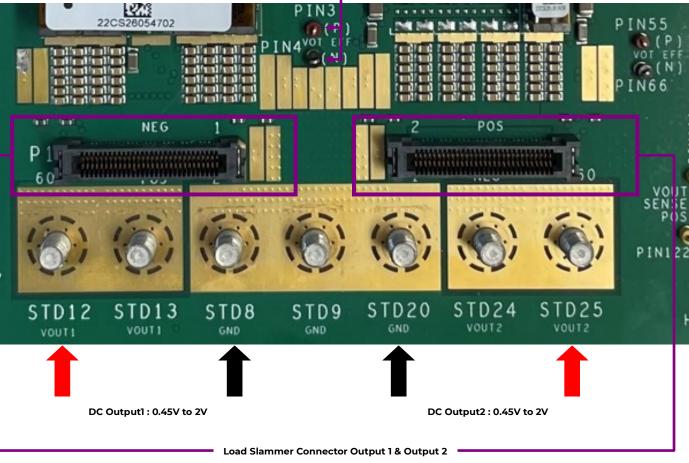
Sense at the output of the POL module (R15, R16, R17, R18) are zero ohm resistors.

Sense at the slammer connector (R10, R11, R12, R14) either zero ohm or 50 ohm.

The single output and the dual output evaluation boards come with the Zero ohm resistors to regulate at the POL. To regulate at the slammer connector remove zero ohm resistors near module and populate at the slammer connector R10, R11, R12, R14.

If the bode plot instrument requires 500hm impedance replace the zero ohm resistors. Use only one pair of zero ohm resistors per output but do not populate both at same time.

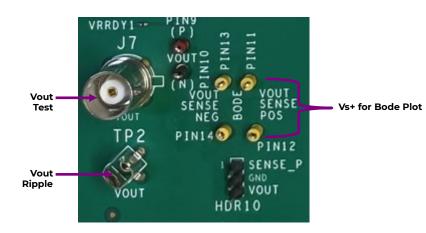


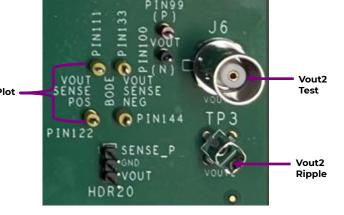


Voutl sense for Efficiency

N3

2.1.3. Load Transient Connections





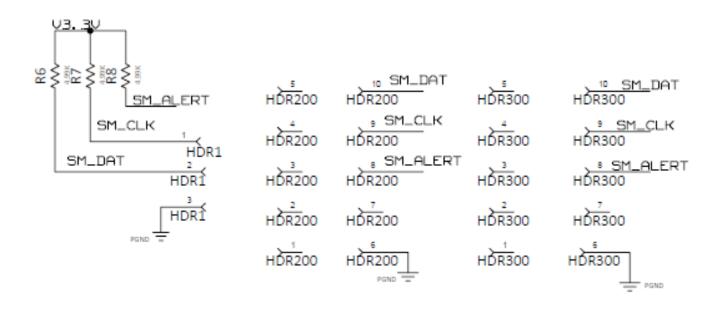


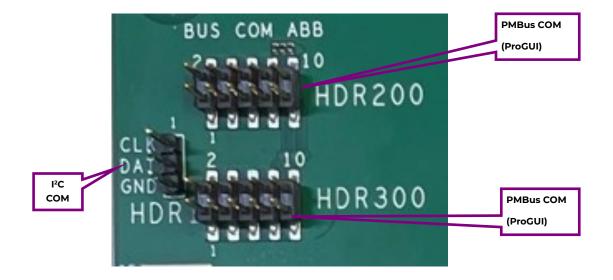
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2.1.4. PMBus Connection

Evaluation Board is provided with a pair of 10 pin connectors and 3 pin header for PMBus connectivity

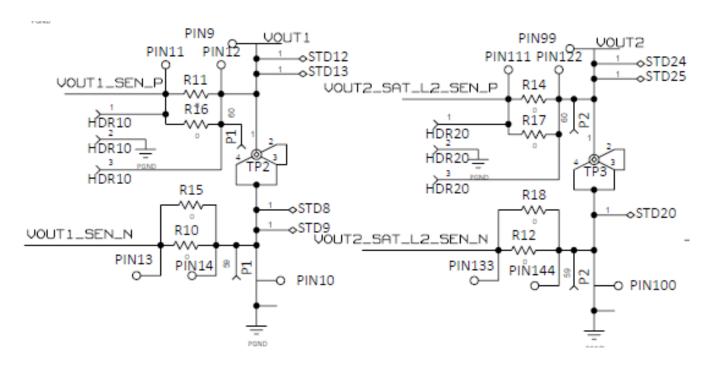




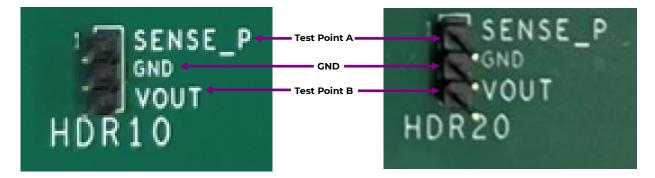


2.1.5. Bode Plot Connection

Evaluation Board is provided with test points for Bode Plot connections. Populate a 10-50 ohm resistor between test points A&B, and inject a small signal across Point A and Point B by using a transformer. Measure voltage of Ch1 (A and GND) and Ch2(B and GND); Gain=Ch1/Ch2.



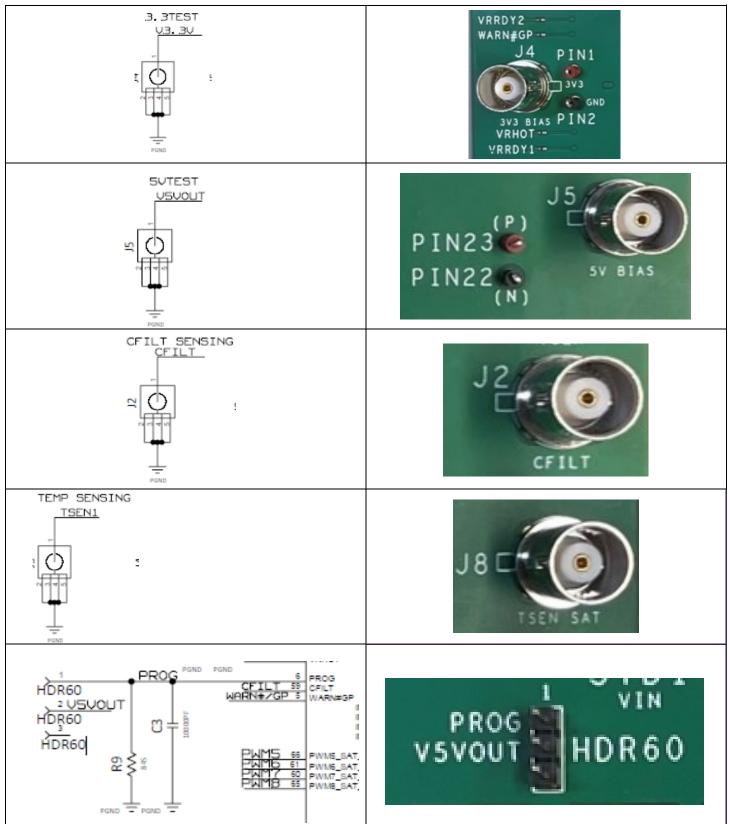
Bode Measurement





2.1.6. Connections Summary

Bias Rails



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2.1.6. Miscellaneous Connections (Continued)

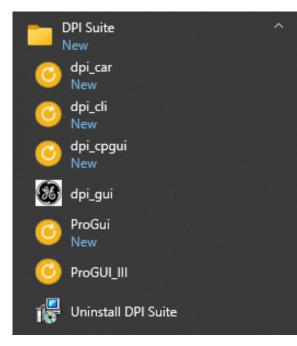
Output Rails

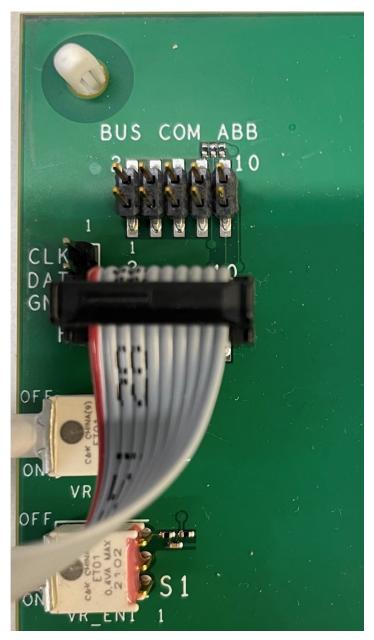
VOUT 1	J7 VOUT SEN TP2 PINT
VOUT 2	PIN99 (P) 00INIdut EG PIN144 TP3 NSE_P DUT



2.2 ProGUI III Connection and Setup

Click on ProGUI_III option after clicking on your Windows Start Icon. Make sure the dongle is connected to the board and the computer. Ensure ribbon cable is connected with the pin alignment shown below.





Click on Connect and then Scan Module to find the MLX module and then click on Start Polling.





• Click on "Module" in the top left corner and then click on Module Configuration.

	Module	Plot	CLI	Log	Setup	Debu
r	Mod	lule Co	nfigur	ation	Ctrl+I	M)e
D	evice 🗸	Conne	ect		Auto Scan	

• A new window will open up. Click on the Confirm button to allow access to the module.

Select Module			
Name MLX160 V	Address 64	✓ Page All	Confirm

• Clicking on the Load Configuration in the Store and Restore section on the Right Upper corner which enables the user to select pre-loaded config files for the type of MLX+SLX board being used.

) Mo	odule Configuration	Dal	ino Control	
Selec	t Module		Read and Write	Store and Restore
Name	MLX160 \vee Address 64 \vee Pa	ge All 🗸 Confirm	Read all Write all ClearFault	Load config Save co
Monit	tor		Status	
_	I			
< 05	(C:) > DPI Suite > DPI Suite > PROGU	II > MLX SLX Combination (Configs	
	(c.) > brisulte > brisulte > ricodol_	II > MEX_SEX_COMDINATION_V	conings	
v folde	er			
es ^	Name	Date modified	Ture	
6	Name	Date modified	Туре	
	MLX040_SLX040_DualVout	7/24/2024 9:17 AM	File folder	
	WIEX040_SEX040_Dualvout	T/ EH/ EOEH STIT HIVI		
в	MLX040_SLX160_DualVout	7/24/2024 9:17 AM	File folder	
B ngi			File folder File folder	
ngi	MLX040_SLX160_DualVout	7/24/2024 9:17 AM		
	MLX040_SLX160_DualVout	7/24/2024 9:17 AM 7/24/2024 9:17 AM	File folder	
igi	MLX040_SLX160_DualVout MLX080_SLX040_DualVout MLX080_SLX160_DualVout	7/24/2024 9:17 AM 7/24/2024 9:17 AM 7/24/2024 9:17 AM	File folder File folder	
igi	MLX040_SLX160_DualVout MLX080_SLX040_DualVout MLX080_SLX040_DualVout MLX080_SLX160_DualVout MLX120_SLX040_DualVout	7/24/2024 9:17 AM 7/24/2024 9:17 AM 7/24/2024 9:17 AM 7/24/2024 9:17 AM	File folder File folder File folder	
gi	MLX040_SLX160_DualVout MLX080_SLX040_DualVout MLX080_SLX040_DualVout MLX080_SLX160_DualVout MLX120_SLX040_DualVout MLX120_SLX160_DualVout	7/24/2024 9:17 AM 7/24/2024 9:17 AM 7/24/2024 9:17 AM 7/24/2024 9:17 AM 7/24/2024 9:17 AM 7/24/2024 9:17 AM	File folder File folder File folder File folder	
ngi	MLX040_SLX160_DualVout MLX080_SLX040_DualVout MLX080_SLX160_DualVout MLX180_SLX160_DualVout MLX120_SLX040_DualVout MLX120_SLX160_DualVout	7/24/2024 9:17 AM 7/24/2024 9:17 AM 7/24/2024 9:17 AM 7/24/2024 9:17 AM 7/24/2024 9:17 AM 7/24/2024 9:17 AM	File folder File folder File folder File folder File folder	
igi	MLX040_SLX160_DualVout MLX080_SLX040_DualVout MLX080_SLX160_DualVout MLX120_SLX040_DualVout MLX120_SLX160_DualVout MLX160_SLX140_DualVout MLX160_SLX040_SingleVout	7/24/2024 9:17 AM 7/24/2024 9:17 AM 7/24/2024 9:17 AM 7/24/2024 9:17 AM 7/24/2024 9:17 AM 7/24/2024 9:17 AM 7/24/2024 9:17 AM	File folder File folder File folder File folder File folder File folder	
ngi	MLX040_SLX160_DualVout MLX080_SLX040_DualVout MLX080_SLX160_DualVout MLX120_SLX040_DualVout MLX120_SLX040_DualVout MLX160_SLX040_DualVout MLX160_SLX040_SingleVout MLX160_SLX040x2_SingleVout	7/24/2024 9:17 AM 7/24/2024 9:17 AM	File folder File folder File folder File folder File folder File folder File folder	

• Select the file from the folder representing the configuration on the board. Be aware that some configurations may have 2 files. Load page 0 followed by Page 1.

OPI Suite > PROGU	_III > MLX_SLX_Combination	on_Configs → MLX160_SLX	040_DualVout	✓ Ö Search
der				
Name	^	Date modified	Туре	Size
160M_40S_D	ualVout_OP_R15_Page0	6/5/2024 11:01 AM	File	4 KB
160M_40S_D	ualVout_OP_R15_Page1	6/5/2024 10:55 AM	File	4 KB
Info	! load configuration will take ter	× 1. .4 n seconds.		
		ОК		



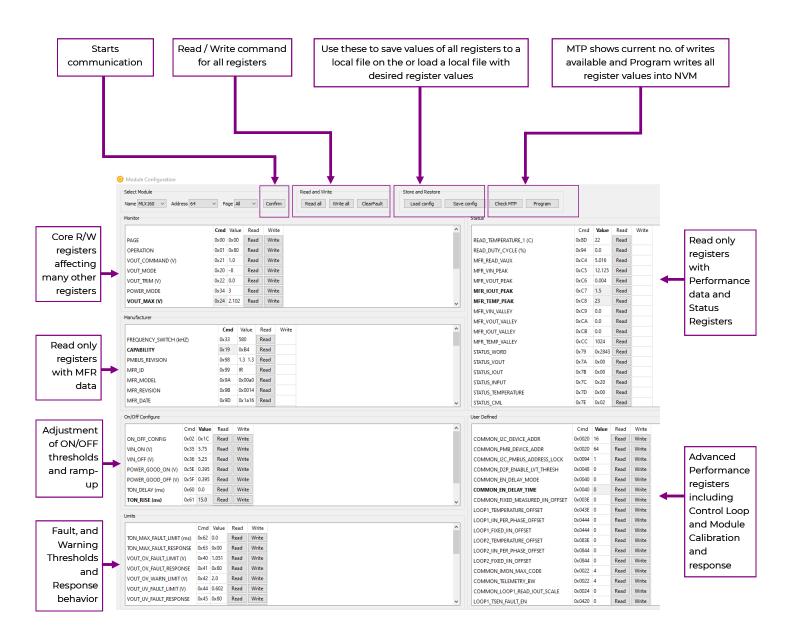
- Clicking on the Hex Command or the Value field for the configurable registers populates the Notes filed on the Right Upper corner which provides the user with information on the available options for that command/register. For example clicking on the current value of 0x80 shows the available valid values for OPERATION command. Remember to click on the Write button after entering the value in the Value register. Click on save config only once all changes have been made since there are limited number of writes available. Another way to conserve number of writes in mentioned later in this document.
- Use PAGE Command to switch between Loop1(Voltage1) and Loop2(Voltage2).

Select Module Name MLX160 V Address 64	∼ Pi	age All	\sim	Confirm	Read a			re and Restore		e config	Chec	k MT	P Program
Monitor							Status					1 🗖	Note
	Cmd	Value	Read	Write	^			Cmd	Value	Read	Write ^		'0X00': 'Normal power-off'
PAGE	0x00	0x00	Read	Write			READ_VIN (V)	0x88	12.125	Read			'0X80': 'On Vout_comman' '0X40': 'Soft OFF(With Sequencing'
OPERATION	0x01	0x80	Read	Write	_	-	READ_IIN (A)	000	0.0	Read		✦	0×94': 'Margin Low IF'
VOUT_COMMAND (V)	0x21	1.0	Read	Write			READ_VOUT (V)	0x8B	0.004	Read			'0X98': 'Margin Low AOF' '0XA4': 'Margin High IF'
VOUT_MODE	0x20	-8	Read	Write			READ_IOUT (A)	0x8C	2.0	Read			'0XA8': 'Margin High AOF'
VOUT_TRIM (V)	0x22	0.0	Read	Write	~		READ_POUT (W)	0x96	0.0	Read			

• Similarly clicking on ON_OFF_CONFIG Value 0x1C data field below brings up all the options available to the user in the Note Section. For example, enter 0x02 if you want module to powerup as soon as input is applied.

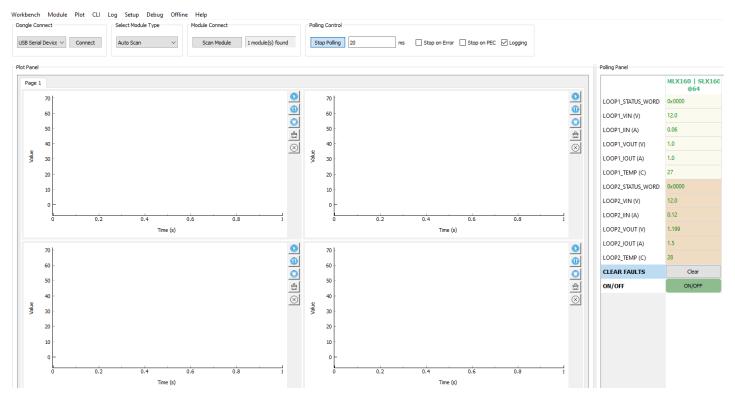
Monitor							Status					Note
	Cmd	Value	e Rea	d Wri	te	^		Cmd	Value	Read	Write ^	'0X02': 'Start when Power
PAGE	0x00	0x00	Rea	d Wri	e		READ_IIN (A)	0x89	0.0	Read		present' '0X14': 'Respond to EN pin
OPERATION	0x01	0x80	Rea	d Wri	te		READ_VOUT (V)	0x8B	0.004	Read		only soft stop
VOUT_COMMAND (V)	0x21	1.0	Rea	d Wri	te		READ_IOUT (A)	0x8C	2.0	Read		active low' '0X15': 'Respond to EN pin
VOUT_MODE	0x20	-8	Rea	d Wri	te		READ_POUT (W)	0x96	0.0	Read		only hard stop
VOUT_TRIM (V)	0x22	0.0	Rea	d Wri	te		READ_PIN (W)	0x97	0.0	Read		active low' '0X16': 'Respond to EN pin
						•	READ_TEMPERATURE_1 (C)	UX8D	23	Read		only soft stop
Manufacturer							READ_DUTY_CYCLE (%)	0x94	0.0	Read		active high' '0X17': 'Respond to EN pin
	C	md	Value	Read	Write	^	MFR_READ_VAUX	0xC4	5.016	Read		only hard stop
COMMON_ISNS_USER_GAIN_PHASE_T	7 0x0	004A ()	Read			MFR_VIN_PEAK	0xC5	12.125	Read		active high' '0X18': 'Respond to Opear
COMMON_ISNS_USER_GAIN_PHASE_8	3 Ox0	004A ()	Read			MFR_VOLIT_PEAK	0xC6	1.0	Read		tion on/off only
IOUT_CAL_OFFSET	0x3	39 -	0.25	Read			MFR_IOUT_PEAK	0xC7	30.0	Read		'0X1C': 'Respond to Opear tion on/off and EN
IOUT_CAL_GAIN (%)	0x3	38 (0.0	Read			MFR_TEMP_PEAK	0xC8	24	Read		pin soft stop active 🗸 🗸
COMMON_DISABLE_OUTPUT	0x0	0040 2	2	Read		~		0.00	00	D	>	Script
On/Off Configure							User Defined					
Cmd Value	Rea	d	/rite			^		Cmd	Value	Read	Write ^	
ON_OFF_CONFIG 0x02 0x1C	Rea	d W	/rite				COMMON_I2C_DEVICE_ADDR	0x0020	16	Read	Write	



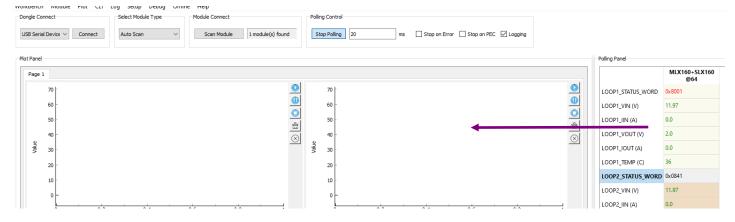




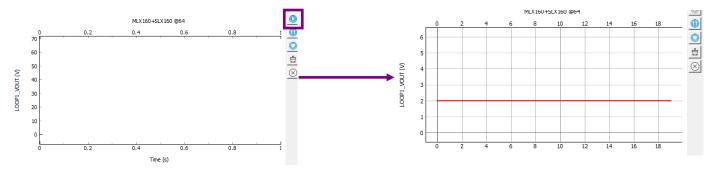
Main Display Screen once Module is On (with output).



Drag desired parameters to screen for polling and drop into graph area.



Remember to click Start.





Once module is Turned On the main screen displays the key input-output measurements.

1	On/Off Configure					
		Cmd	Value	Read	Write	
	ON_OFF_CONFIG	0x02	0x02	Read	Write	

Polling Panel							
	MLX160 SLX160 @64						
LOOP1_STATUS_WORD	0x0000						
LOOP1_VIN (V)	12.0						
LOOP1_IIN (A)	0.03						
LOOP1_VOUT (V)	1.0						
LOOP1_IOUT (A)	1.0						
LOOP1_TEMP (C)	28						
LOOP2_STATUS_WORD	0x0000						
LOOP2_VIN (V)	12.0						
LOOP2_IIN (A)	0.16						
LOOP2_VOUT (V)	1.199						
LOOP2_IOUT (A)	1.5						
LOOP2_TEMP (C)	29						
CLEAR FAULTS	Clear						
ON/OFF	ON/OFF						



Revision History

Revision	Date	Description of the change
1.1	02/06/2024	Initial Release
1.2	08/07/2024	Guidelines for use with ProGUI III added
1.3	05/09/2025	Added note regarding PGOOD on cover poge



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