

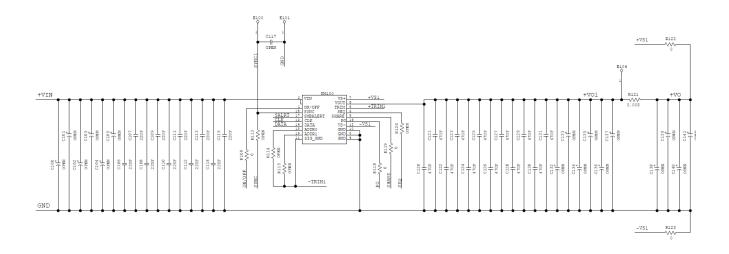
# Analog and Digital DLynx<sup>TM</sup>: Non-Isolated DC-DC Power Modules

**40A Output MegaDLynx<sup>™</sup> paralleling board** 

# MegaLynx<sup>™</sup> Paralleling Evaluation Board Documentation

The MegaDLynx board has a single layout of 3 MegaDLynx modules and is not intended for cross-use with other modules. The board comes with a module already assembled on to the board, test points and also some amount of input and boutput filtering. Users should refer to the MegaDLynx datasheet for information on features, selecting output capacitance, tunable loop values and instructions on paralleling the modules

## 1.Schematics



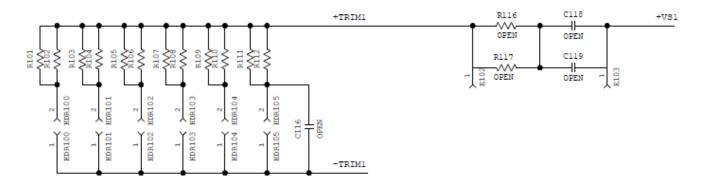
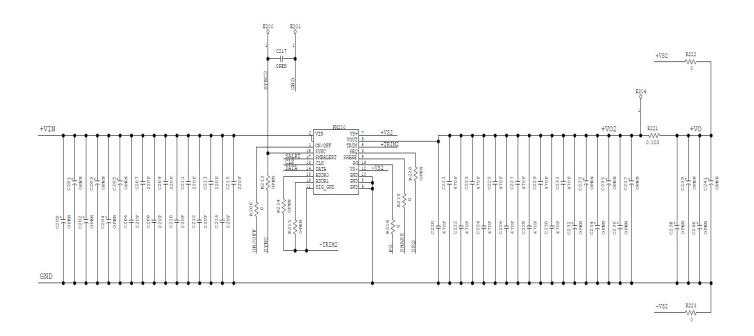


Figure 1a. Schematic of the MegaDLynx Paralleling Evaluation board.





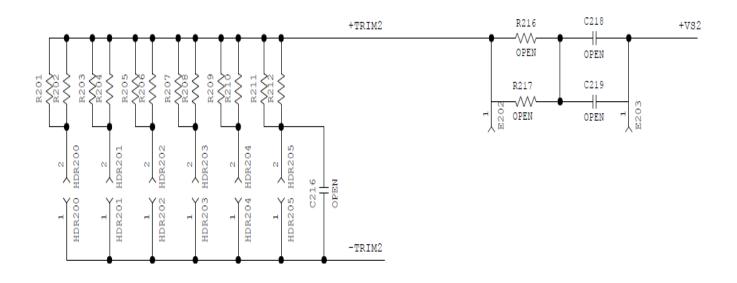
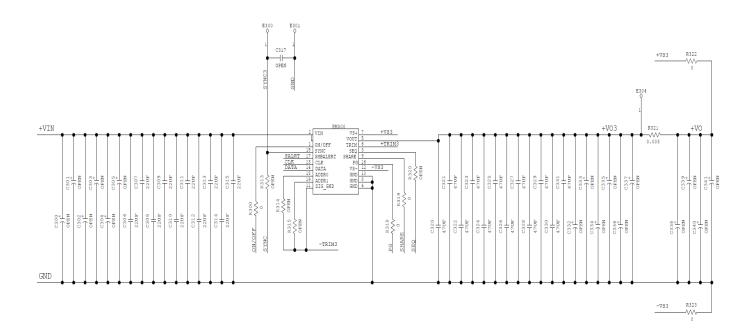


Figure 1b. Schematic of the MegaDLynx Paralleling Evaluation board.





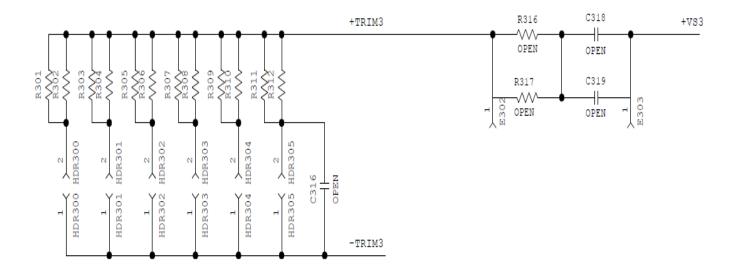


Figure 1c. Schematic of the MegaDLynx Paralleling Evaluation board.



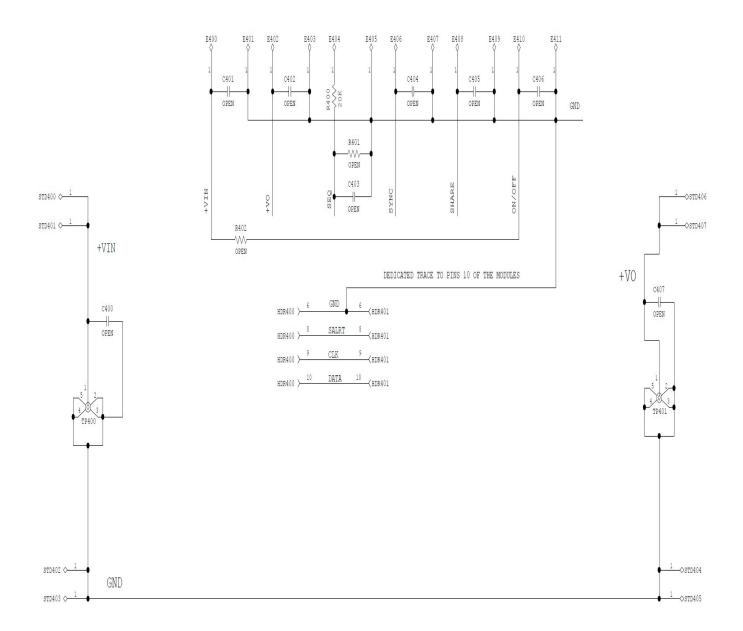


Figure 1d. Schematic of the MegaDLynx Paralleling Evaluation board.



# 2. Physical Descriptions

An annotated photograph of the MegaDLynx<sup>TM</sup> paralleling evaluation board is provided in the figure below. The notes indicate locations of various components. A minimum list of external components are input filtering ( $(2x0.01\mu\text{F} + 5 \times 22\mu\text{F}$ ,  $16V_{min}$  ceramic capacitors+ $2x470\mu\text{F}$  electrolytic are recommended as a minimum per module are already assembled on the board) and some amount of output filtering ( $2x0.01\mu\text{F} + 6x47\mu\text{F}$  ceramic,  $4V_{min}$ ). Please refer to module datasheet for module pad layout information and minimum specified capacitance and recommendations for Tunable Loop values ( $R_{tune}$ ,  $C_{tune}$ ). The availability of an external Sync signal is mandatory for this board. See paralleling section in datasheet.

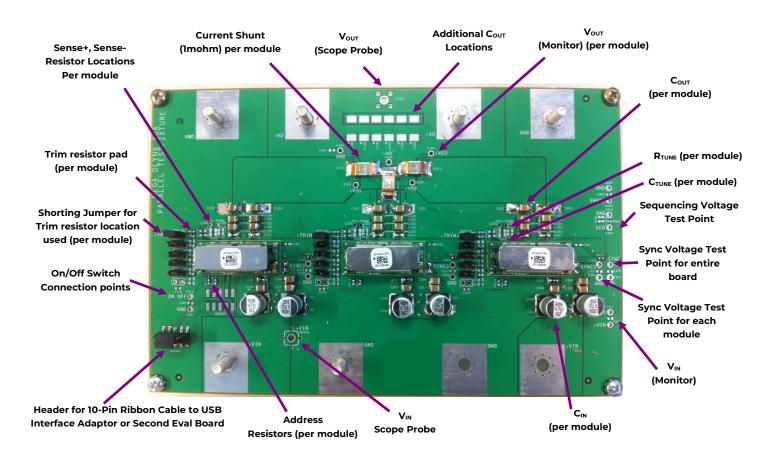


Figure 2a – Top View MegaDLynx™ Paralleling evaluation board

**Caution!** Before applying power, make sure that the unit under test and the externally installed capacitors (input & output) have appropriate voltage ratings.

Rev. 1.0



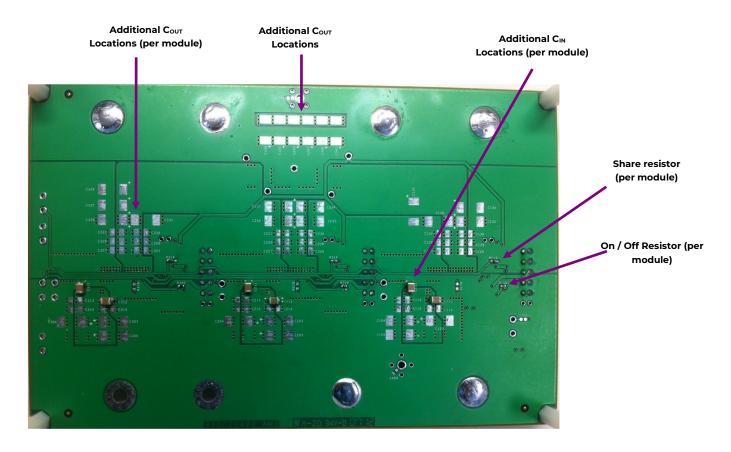


Figure 2b – Bottom View MegaDLynx™ Paralleling evaluation board

**Caution!** Before applying power, make sure that the unit under test and the externally installed capacitors (input & output) have appropriate voltage ratings.

# **Contact Us**

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